

Attorney's Docket No.: 10559-127002/P7866C *IFW*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Wolrich et al.
Serial No.: 10/780,330
Filed : February 17, 2004
Assignee : Intel Corporation
Title : MEMORY MAPPING IN A MULTI-ENGINE PROCESSOR

Art Unit: 2127
Examiner:

Mail Stop Amendment

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

Copies of the U.S. patents/publications are not being provided per the waiver posted on the PTO website.

This filing is being made before the receipt of a first Office action on the merits. No fee is required.

The documents are in the English language; hence no concise explanation is necessary per Rule 98(a)(3).

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

May 11, 2005
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Page 2 of 2

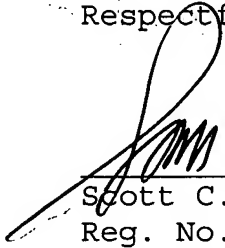
Attorney's Docket No.:10559-127002

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: May 11, 2005

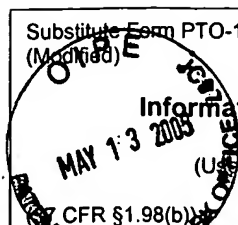


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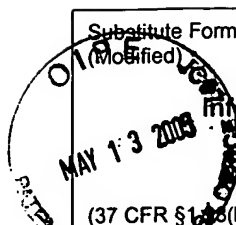
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Substitute Form PTO-1449 (Modified) 	U.S. Department of Commerce Patent and Trademark Office		Attorney's Docket No. 10559-127002	Application No. 10/780,330
	Applicant Wolrich et al.			
	Filing Date February 17, 2004		Group Art Unit 2127	

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	4,016,548	04/1977	Law, et al.			
	AB	4,032,899	06/1977	Jenny, et al.			
	AC	4,075,691	02/1978	Davis, et al.			
	AD	4,788,640	11/1988	Hansen			
	AE	4,831,358	05/1989	Ferrio, et al.			
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	AH	5,568,476	10/1996	Sherer, et al.			
	AI	5,701,434	12/1997	Nakagawa			
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	AK	5,764,915	06/1998	Heimsoth, et al.			
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	AR	6,111,886	08/2000	Stewart			
	AS	6,170,051	01/2001	Dowling			
	AT	6,212,542	04/2001	Kahle, et al.			
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	AZ	6,463,072	10/2002	Wolrich, et al.			
	AAA	6,463,480	10/2002	Kikuchi, et al.			

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Form PTO-1449 (Modified)  Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.102(b))	U.S. Department of Commerce Patent and Trademark Office		Attorney's Docket No. 10559-127002	Application No. 10/780,330
	Applicant Wolrich et al.			
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U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	ABB	6,463,527	10/2002	Vishkin			
	ACC	6,466,898	10/2002	Chan			
	ADD	6,529,983	03/2003	Marshall, et al.			
	AEE	6,535,878	03/2003	Guedalia, et al.			
	AFF	6,584,522	06/2003	Wolrich, et al.			
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	AHH	6,604,125	08/2003	Belkin			
	AII	6,606,704	08/2003	Adiletta, et al.			
	AJJ	6,625,654	09/2003	Wolrich, et al.			
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	ALL	6,668,317	12/2003	Bernstein, et al.			
	AMM	6,671,827	12/2003	Guilford, et al.			
	ANN	6,792,488	09/2004	Wolrich, et al.			

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AOO							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	APP	"10-/100-Mbps Ethernet Media Access Controller (MAC) Core", NEC, 1998, pages 1-5.
	AQQ	"Nomadic Threads: A migrating multithread approach to remote memory accesses in multiprocessors", by Jenks, S.; Gaudiot, J.L. (abstract only) Publication Date: 20-23 Oct. 1996.
	ARR	"Overview of the START (*T) multithreaded computer" by Beckerie, M.J. (abstract only) Publication Date: 22-26 Feb. 1993.
	ASS	A. Ippoliti, et al., "Parallel Media Access Controller for Packet Communications at Gb/s Rates", 1990, IEEE, pages 991-996.
	ATT	Howard Frazier, "Gigabit Ethernet: From 100 to 1,000 Mbps", 1999, IEEE Internet Computing, pages 24-31.
	AUU	Howard Frazier, "The 802.3z Gigabit Ethernet Standard", 1998, IEEE Network, pages 6-7.

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